AUTOMATIC POWER FACTOR CORRECTION RELAY



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DIGITAL MICROCONTROLLER BASED INTELLIGENT AUTOMATIC POWER FACTOR CONTROLLER/ REGULATOR/ CORRECTION RELAY(APFCR)→PNPF SERIES IEEE DEVICE CODE-55

Feautures

- · Feeder KVAR entry through Keyboard.
 - Fine tuning possible (independent of ratio's)
 - absolute value of KVAR
- User entered parameter is stored in non-volatile memory (EEprom)
- CT Ratio & System Voltage user selectable through key board display.
- Independent of Frequency.
- Most Optimum combination obtained through advanced control programming.
- RISC architecture dedicated Microcontroller based.
- Target pf 0.80lag to 0.90 lead selectable through key board display.
- Auto/Manual mode.
- No complicated setting procedure.
- · User friendly interface.
- Single line 16 character back-lit LCD display for P.F & entering parameters and LED indication for capacitor bank status.
- Software set dead band for hunt-free operation.
- Display indication for PT failure.
- Display of reversal of polarity of CT.
- Under current indication.

Applications

- •Industrial application where inductive load which are critical like inching load & furnaces, i.e. Dynamic loads.
- •Application wherever energy conservation is vital.

Specifications

- Current: CT secondary, 5Amps, CT can be mounted on any phase of the
 - main incomer (1Amp model is available on request)
- Voltage : Built in PT, can be connected between remaining two

phases

Aux. Supply : 85-275V AC/DC

Indication : 16 Character back-lit LCD Display.

CT Burden : 0.25VA

• Capacitor stage: 4,6,8, 10 & 12 stages

• Switching time : 60 Seconds

• Dimensions :144 x 144 x 120 mm (H x W x D)

• Panel Cut-out : 137 x 137mm

Size







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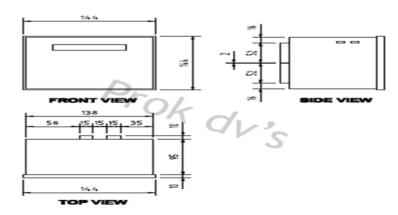
Introduction

RISC architecture based, intelligent state-of-the-art PNPF Prok dv's make Digital Microcontroller based Intelligent Automatic Power Factor Controller/Regulator/Correction Relay (APFCR) with on-line monitoring of line PF through industrial grade back-lit LCD Display. The Unit uses one of the tried and time tested intelligent algorithms to switch the capacitor banks, in a most optimum combination in a shortest time for any dynamic load conditions without the need to select the complicated switching sequences by the user.

Unlike the present existing PFCR, the user has a free choice to select and connect any KVAR on any feeder, thereby user need not connect the bank in ascending or descending order as recommended/practiced by other available makes, Target P.F. can be achieved by less number of switching of capacitor banks and feeder operations and thus capacitor banks can be connected to the system based on the load pattern. Digital Microcontroller based Intelligent Automatic Power Factor Controller/Regulator/Correction Relay (APFCR) will work effectively when the load is as less as 5 % (approximately) and displays a message viz low current if load is less than 5% and also displays messages like reversal of CT and PT to indicate the wrong connection of CT/PT at the mains input Digital Microcontroller based Intelligent Automatic Power Factor Controller/Regulator/Correction Relay (APFCR) switches on/off the necessary capacitor banks depending upon the nature and dynamics of load and its patterns, the target power factor is achieved effectively. Digital Microcontroller based Intelligent Automatic Power Factor Controller/Regulator/Correction Relay (APFCR) enhances the endurance of capacitors and contactors for the reason optimal and necessary switching off/on capacitor banks.

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DIMENSIONS INTELIGENT POWER FACTOR CORRECTION RELAY - IPFC



NOTE: ALL DIMENSIONS ARE IN MM







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NOTE: APFCR CT TO BE CONNECTED IN INCOMING SIDE





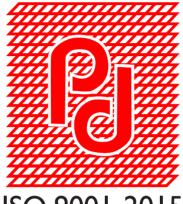


AUTO SOURCE



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